

Examiner Signature	/Khai Tran/	Date Considered	11/08/2000
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<p>Substitute for form 1449A/PTO</p> <h1 style="text-align: center;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT</h1> <p style="text-align: center;"><i>(use as many sheets as necessary)</i></p>				Complete if Known	
Sheet	1	of	2	Application Number	09/954,915
				Filing Date	September 17, 2001
				First Named Inventor	Mark Peting
				Art Unit	2661
				Examiner Name	Unassigned
				Attorney Docket Number	4259P044

U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

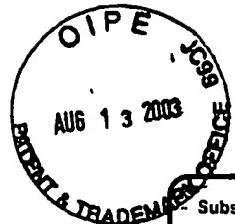
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet

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of

2

Complete if Known

Application Number	09/954,915
Filing Date	September 17, 2001
First Named Inventor	Mark Petting
Art Unit	2661
Examiner Name	Unassigned
Attorney Docket Number	4259P044

OTHER ART - NON PATENT LITERATURE DOCUMENTS

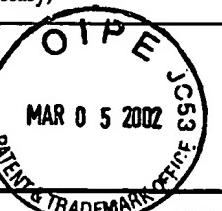
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
KT		WH YIM and F P COAKLEY, On-Board Processing For KA-Band Applications, University of Surrey, UK, Publication Date, February 11, 1993.	
KT		MARIE-LAURE BOUCHERET, et al., Fast Convolution Filter Banks For Satellite Payloads With On-Board Processing, IEEE Journal on Selected Areas in Communications, Vol. 17. No.2, February 1999.	RECEIVED AUG 14 2003
			Technology Center 2600

Examiner Signature	/Khai Tran/	Date Considered	11/08/2006
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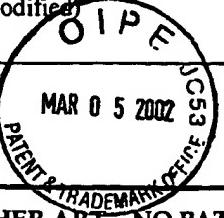
Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)		Attorney Docket No.: 04259.P044	Application Number: 09/954,915		
 O P E MAR 05 2002 JCS <small>PATENT & TRADEMARK OFFICE, WASHINGTON, DC</small>		First Named Inventor: Mark Petting	RECEIVED		
		Filing Date: September 17, 2001	MAR 08 2002		
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U.S. PATENT DOCUMENTS					
Exam. Initial*	Cite No.*	U.S. Patent Document Number	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
KT		6,148,043	Fujimoto	11/14/2000	
		6,141,391	Morelos-Zaragoza, et al.	10/31/2000	
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		6,119,265	Hara	09/12/2000	
		5,974,095	Kitaura, et al.	10/26/1999	
		6,195,642	Izumi, et al.	02/27/2001	
		6,122,325	Mogre, et al.	09/19/2000	
V		6,049,573	Song	04/11/2000	
KT		6,005,640	Stolle, et al.	12/21/1999	

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Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)		Attorney Docket No.: 04259.P044	Application Number: 09/954,915	
		First Named Inventor: Mark Petting	RECEIVED MAR 08 2002	
		Filing Date: September 17, 2001		
Technology Center 2600				
OTHER ART - NO PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published		Translation ²
KT		JAMES TSUI, Frequency Channelization, Digital Techniques for Wideband Receivers, Second Edition, Pages 363 – 396, 2001 Artech House, Inc., Norwood, MA		
		ZHENGDIAO WANG and GEORGIOS B. GIANNAKIS, Wireless Multicarrier Communications where Fourier Meets Shannon, Department of ECE, University of Minnesota, Minneapolis MN., Pages 1-21		
		E. VERRIEST, ISEN, Implementing an Adaptive Noise Canceling System to Enhance Sonar Receiver Performance Using the TMS320C31 DSP, ESIEE, Paris, September 1996, Texas Instruments, Pages 1-24.		
		G.A. SHAW, R.A. FORD, J.C. ANDERSON, B.W. ZUERDNORFER, A.H. ANDERSON, RASSP Benchmark 2 Technical Description, Massachusetts Institute Of Technology Lincoln Library, 153 pages total.		
		"www.inventra.com/inventra/softcore/workshop/MultiRaFiltDes95/" MENTOR GRAPHICS, Hardware Design of Decimators/Interpolators, Pages 1-38.		
		"www.mentor.com/inventra/softcore/workshop/SDmod95/", MENTOR GRAPHICS, Introduction to AD/DA Converters, Pages 1-27.		
		http://www.mentor.com/inventra/softcore/workshop/SDHWDes95/ MENTOR GRAPHICS, Design of the Decimation & Interpolation Filters, Pages 1-57.		
KT		<u>http://www.mentor.com/inventra/softcore/workshop/Applications95/</u> , MENTOR GRAPHICS, Sigma Delta Converter Applications, Pages 1-5.		

Examiner Signature	/Khai Tran/	Date Considered	11/08/2006
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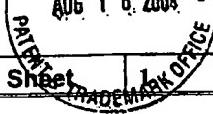
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Substitute for Form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(use as many sheets as necessary)



Sheet 1

of 2

Complete if Known

Application Number	09/954,915
Filing Date	September 17, 2001
First Named Inventor:	Mark Petting
Art Unit	2661
Examiner Name	Not Yet Assigned

Attorney Docket Number 004259.P044

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
KT		us- 4,015,238		3/29/1977	Robert Curtis Davis	RECEIVED
		us- 5,027,374		6/25/1991	Mark W. Roszman	AUG 17 2004
		us- 5,291,499 A		3/1/1994	Behrens et al.	Technology Center 2600
		us- 5,295,142		3/15/1994	Izumi Hatakeyama	
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		us- 6,278,725 B1		8/21/2001	Rouphael et al.	
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KT		us- 6,563,889 B1		5/13/2003	Shih, et al.	
		us-				
		us-				
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
KT		WO 01/59937 A2	8/16/2001	Qualcomm Inc.		

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Substitute for Form 1449/PTO				Complete If Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <small>(use as many sheets as necessary)</small> <small>AUG 16 2004</small>				Application Number	09/954,915
				Filing Date	September 17, 2001
				First Named Inventor:	Mark Petting
				Art Unit	2661
				Examiner Name	Not Yet Assigned
Sheet	2	of	2	Attorney Docket Number	004259.P044 Technology Center 2600
NON PATENT LITERATURE DOCUMENTS					
Examiner Initials ¹	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T ²
KT		BREE, ET AL., "A Bit-Serial Architecture For A VLSI Viterbi Processor", Communications Systems Research Group, University of Saskatchewan, Saskatoon, IEEE, WESCANEX '88, 1988, pages 72-77.			
		BIVER, ET AL., "Architectural Design and Realization Of A Single-Chip Viterbi Decoder", Elsevier Science Publishers B.V., INTEGRATION, The VLSI Journal 8 (1989), October, No. 1, Amsterdam, NL, Pages 3-16.			
		BREE, ET AL., "A Modular Bit-Serial Architecture For Large Constraint-Length Viterbi Decoding", Communications Systems Research Group, University of Saskatchewan, Saskatoon, Canada, IEEE International Conference on Communications", 1990, pages 1501-1506.			
		CHOI, ET AL., "Viterbi Detector Architecture For High-Speed Optical Storage", 1997, IEEE TENCON - Speech and Image Technologies for Computing and Telecommunications, ASIC Center Corporate Technical Operations SAMSUNG Electronics, Vol. 1, Dec 1997, pages 89-92.			
		W.H. YIM and F.P. COAKLEY, "On-Board Processing For KA-Band Applications", University of Surrey, UK, Publication Date, February 11, 1993., XP 000458011, pp. 225-229.			
KT		HASHIDA MITSUYOSHI, "Hierachical Network Management System and Control Method for Network Management Information," Patent Abstracts of Japan, Publication No. 07226777			
Examiner Signature	/Khai Tran/			Date Considered	11/08/2006

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